

AD05 DVB-T SFN Adapter



Function:

DVB-T SFN Adapter fully comply with TS-101191, with two separate channels can support HP and LP when Hierarchy Modulation Mode



Overview:

AD05 DVB-T SFN Adapter periodically generates and inserts MIP packet in the MPEG transport stream to synchronize the signal from all transmitters in the terrestrial digital TV SFN. The equipment has two channels MPEG TS input and output. It can be configured as two channel adapter to use independently, as well as backup for each other. Moreover, it can adjust the output bit-rate of TS automatically, according to the TPS signal. It is the essential equipment for establishing terrestrial digital TV SFN.



Features:

- Completely comply with TS-101191 standard
- Support DVB-T and DVB-H modes, including level modulation
- Support 5, 6, 7, 8MHz bandwidth
- ASI input interface auto-switching, bit-rate adaptive
- Non-level mode: ASI backup output; level mode: HP and LP output
- Level mode double input stream (HP and LP) MIP insertion



Technical Specification:

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|-------------------------|--|---|
| » MPEG-TS Input | » 2 channels ASI input, complying with EN50083 ASI interface standard | |
| | » Non-level modulation mode: 2 channels ASI hot standby input. | |
| | » Level modulation mode: support HP and LP inputs | |
| | » Support 188 packet without RS code and 204 packet with RS code | |
| | » Support bit-rate adapting and PCR correction | |
| » MPEG-TS Output | » BNC, with 75Ω impedance | |
| | » 2 channels ASI output, complying with EN50083 ASI interface standard | |
| | » Non-level modulation mode: Outputting the same 2 channels TS, supporting MIP insertion | |
| | » Level modulation mode: Supporting MIP insertion of 2 channels TS (HP and LP) | |
| | » Support 188 packet without RS code and 204 packet with RS code | |
| » MIP Processing | » BNC, with 75Ω impedance | |
| | » Time stamp up to 100ns | |
| | » Support 5, 6, 7, 8MHz bandwidth | |
| » Control and Monitor | » The adjusting range of network delay is 0~1s | |
| | » Panel Monitor | |
| » Clock Synchronization | » WEB page control | |
| | » TCXO | |
| | » Internal 10MHz clock: 0.5ppm | |
| | » External 10MHz reference input: -5~+10dBm | |
| | » Connector Specification: BNC, with 50Ω impedance | |
| | » External 1pps reference input | |
| | » Reference Level: TTL level with 5KΩ impedance | |
| | » Reference minimum pulse width: 1μ s | |
| | » Calculate and insert all information of MIP packet | |
| | » Establishing mega frame structure and calculating the sync information from GPS | |
| » Physical Parameter | » Rated Voltage | » AC90-260V 50/60Hz |
| | » Rated Power | » 30W |
| | » Operation Temperature | » -10~50 |
| | » Storage Conditions | » -10~70 |
| | » Dimension | » Depth: 318mm, Width: 483mm, Height: 44mm (1U) |
| | » Weight | » 5kg |